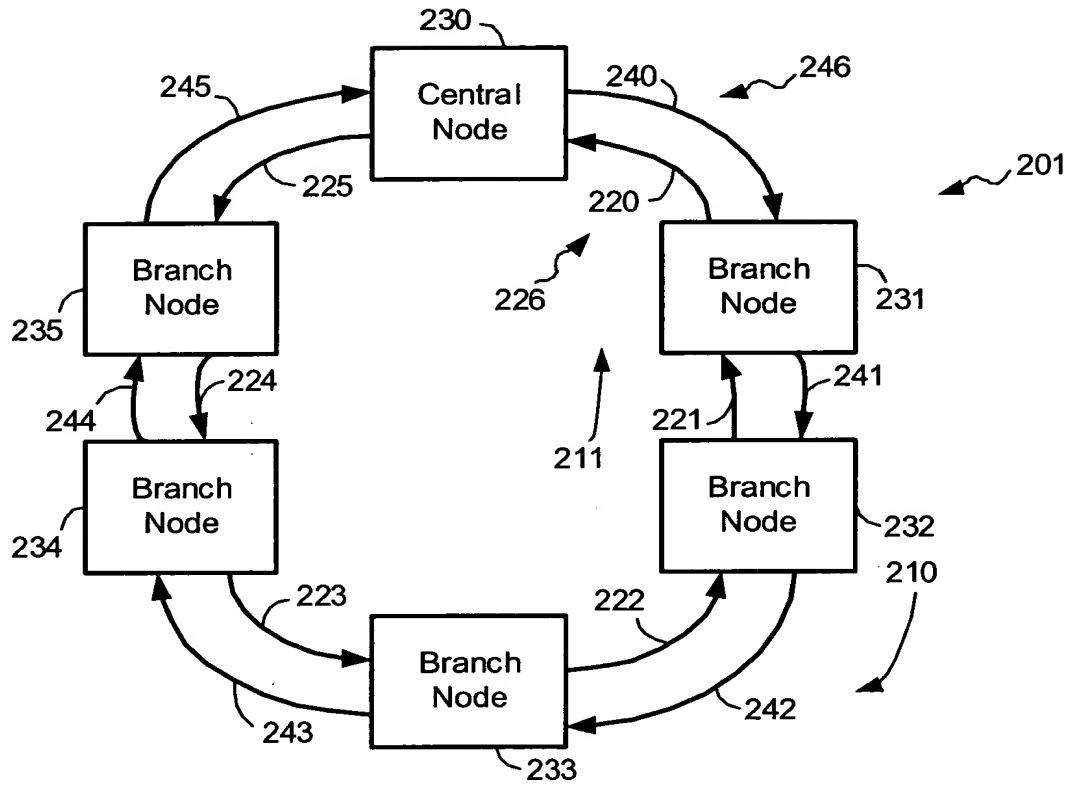
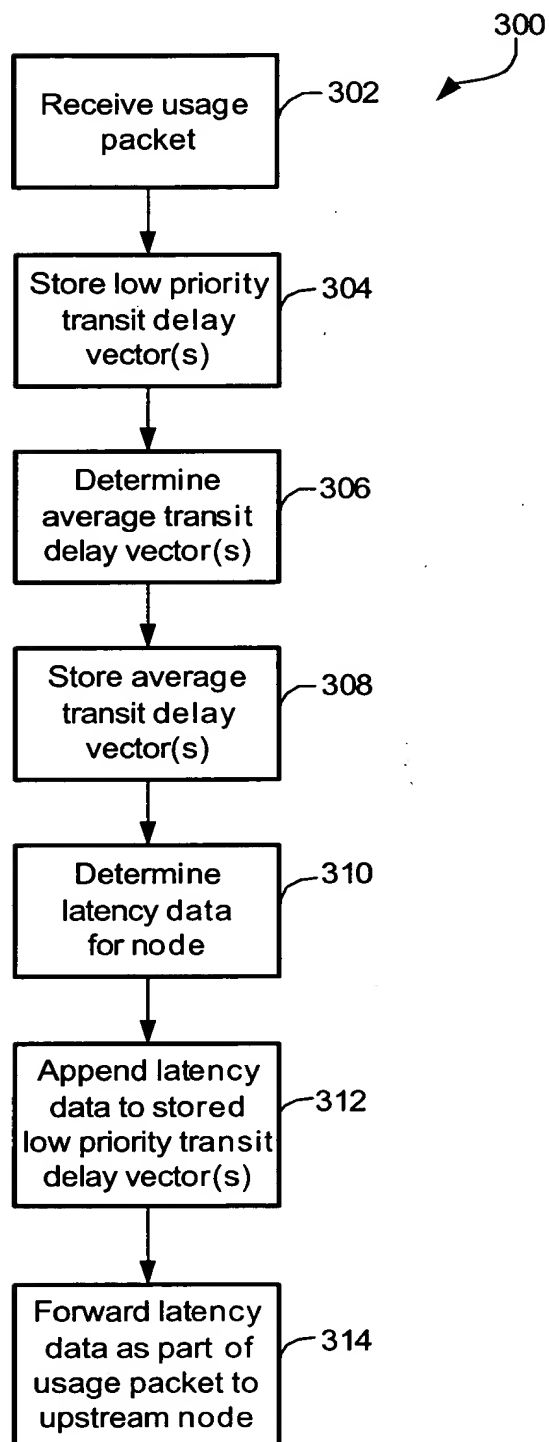


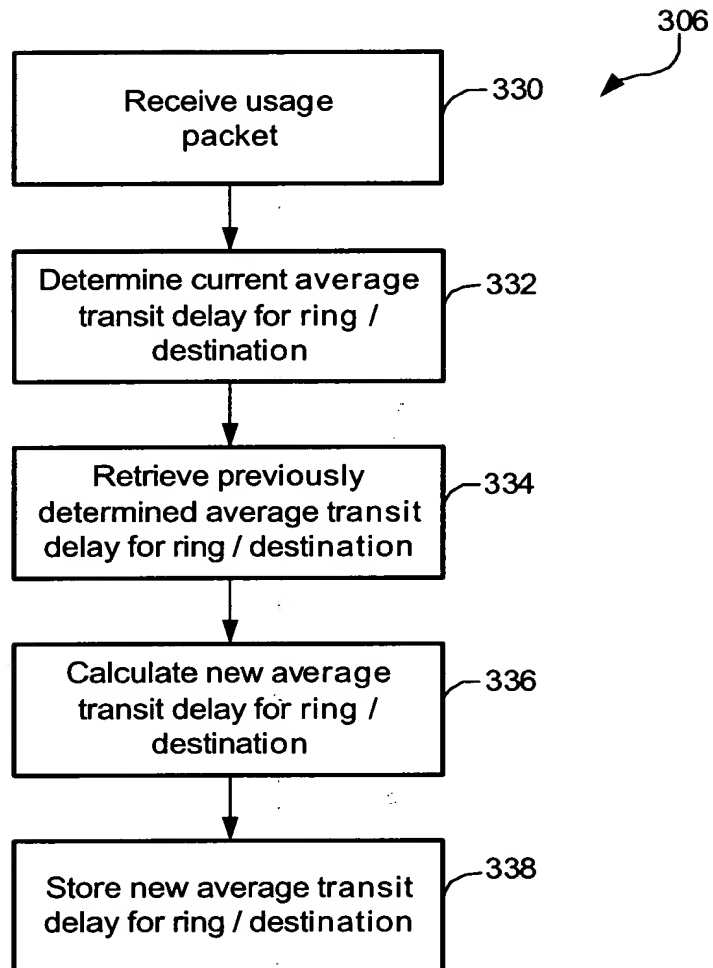
**Fig. 1**



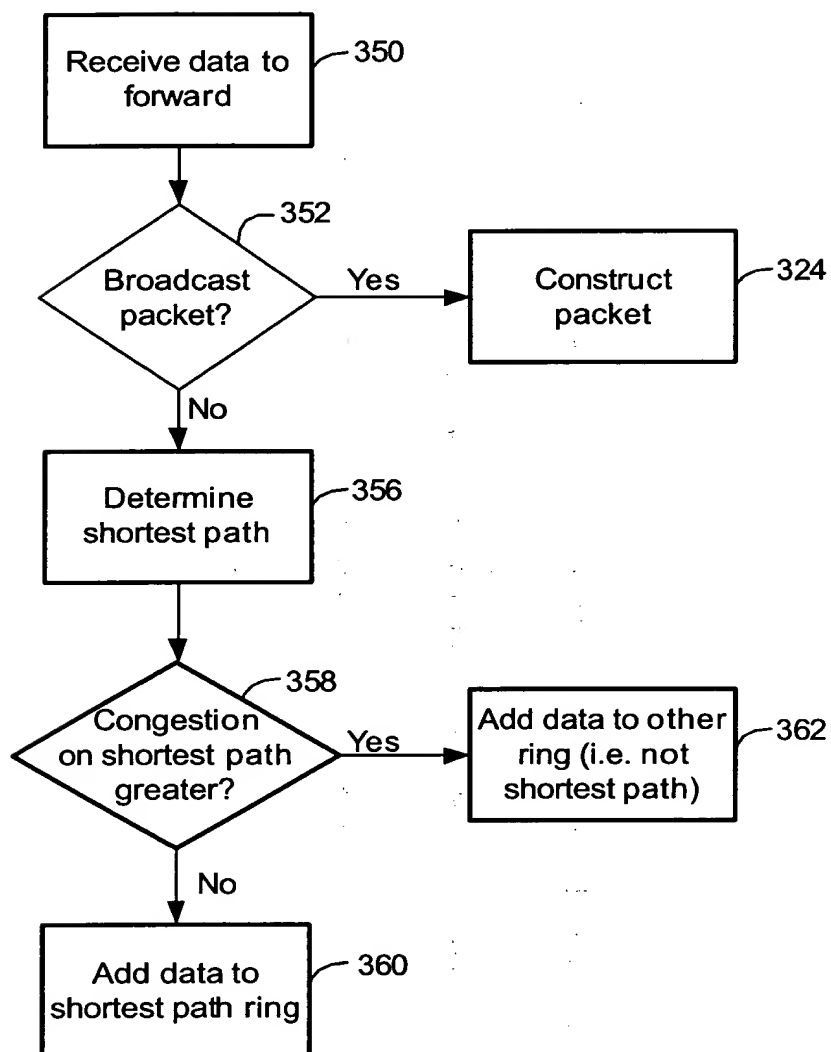
**Fig. 2**



**Fig. 3a**

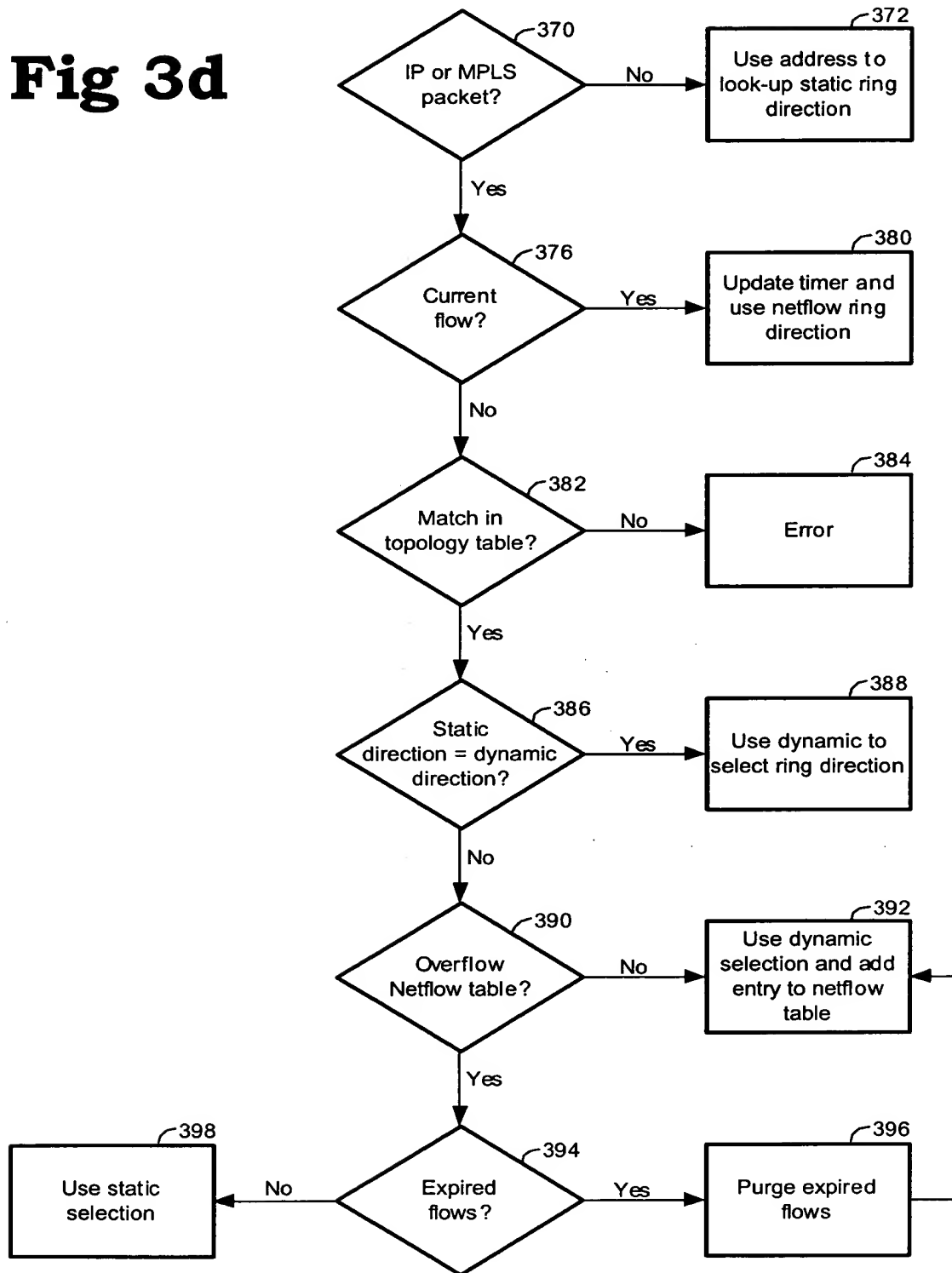


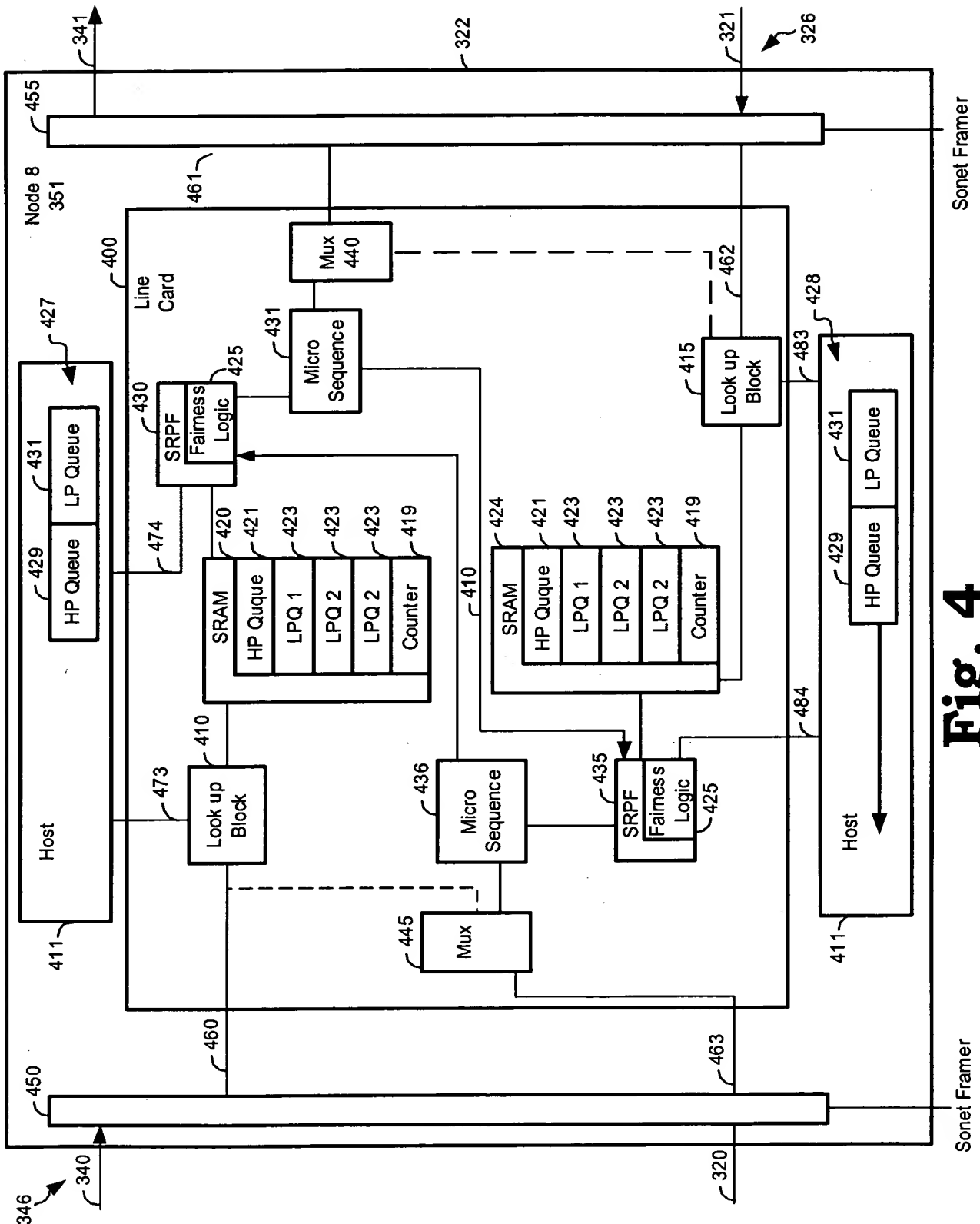
**Fig. 3b**



**Fig. 3c**

**Fig 3d**





**Fig. 4**

502	504	506	508	510	512	514
Address (MAC, IP)	HOP Count Inner	HOP Count Outer	Latency Inner	Latency Outer	Static Ring Direction	Dynamic Ring Direction
12, 23, 46	10	8	1 2 3 4	1 4 5 6	I	O
12, 23, 47	9	9	1 2 0 0	1 4 0 0	I	I

**Fig. 5**